

ABSTRACT OF THE DISCLOSURE

A semiconductor integrated circuit is provided, which comprises a first cell comprising a plurality of 5 transistors, a second cell comprising a PMOS transistor section and an NMOS transistor section, the PMOS transistor section comprising a first PMOS transistor and a second PMOS transistor connected to the first PMOS transistor in series, the NMOS transistor section comprising a first 10 NMOS transistor and a second NMOS transistor connected to the first NMOS transistor in series. A predetermined scheme is used to connect between the first cell and the second cell, between the plurality of transistors in the first cell, and between the PMOS transistor section and 15 the NMOS transistor section in the second cell.